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PATENT APPLICATION  
Do. No. 3981-33

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Joel R. Goergen

Serial No.: 10/068,420

Examiner: Not yet assigned

Filing Date: February 5, 2002

Group Art Unit: 2841

Title: HIGH-SPEED ELECTRICAL ROUTER BACKPLANE WITH NOISE-  
ISOLATED POWER DISTRIBUTION

May 6, 2002

Box Patent Application  
ASSISTANT COMMISSIONER FOR PATENTS  
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### INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted under 37 CFR 1.97 (b).

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.

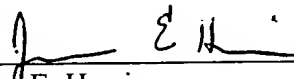


20575

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Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

  
James E. Harris  
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CP/2841

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In re patent application of: Joel R. Goergen

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Filing Date: February 5, 2002

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TRANSMITTAL LETTER

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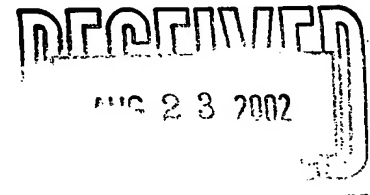
Enclosed for filing in the above-referenced application are the following:

- ☒ Information Disclosure Statement  
☒ PTO Form 1449 ☒ References cited  
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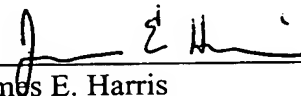
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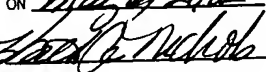
Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.



James E. Harris  
Reg. No. 40,013

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Applicant: Joel R. Goergen

Serial No. 10/068,420

Filing Date: February 5, 2002

Group: 2841

**INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)**

**U.S. PATENT DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub- Class</u>
_____	_____	6,081,430	06/27/00	La Rue		
_____	_____	5,010,641	04/30/91	Sisler		
_____	_____	5,870,274	02/09/99	Lucas		

**FOREIGN PATENT DOCUMENTS**

<u>Document Number</u>	<u>Publication Date</u>	<u>Country</u>	<u>Name</u>
_____	_____	None	

**OTHER DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, etc.)</u>
_____	_____	1 Teradyne: Web ProForum Tutorial; <u>Design Considerations for Gigabit Backplane Systems Tutorial</u> printed on November 6, 2000 from website located at <a href="http://www.iec.org/tutorials/design_backplane/">www.iec.org/tutorials/design_backplane/</a> ; pp. 1-43
_____	_____	2 Teradyne: VHDM Home Page; <u>Backplane Assemblies and Connectors</u> printed on November 6, 2000 from website located at <a href="http://www.teradyne.com/prods/bps/vhdm/intro.html">www.teradyne.com/prods/bps/vhdm/intro.html</a> ; pp. 1-3
_____	_____	3 Teradyne: Products; <u>Backplanes</u> printed on November 6, 2000 from website located at <a href="http://www.teradyne.com/prods/bps/vhdm/intro.html">www.teradyne.com/prods/bps/vhdm/intro.html</a> ; pp. 1-2
_____	_____	4 Teradyne: VHDM Home Page; <u>VHDM High-Speed Differential</u> printed on November 6, 2000 from website located at <a href="http://www.teradyne.com/prods/bps/vhdm/intro.html">www.teradyne.com/prods/bps/vhdm/intro.html</a> ; pp. 1-3

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5 Teradyne: Virtual Exhibits; Total Interconnect Solutions printed on November 6, 2000 from website located at [www.iec.org/exhibits/teradyne\\_02/](http://www.iec.org/exhibits/teradyne_02/); pp. 1-5

6 Cahners EDN Access; New printed-wiring-board materials guard against garbled gigabits printed on November 6, 2000 from website located at [www.ednmag.com/ednmag/reg/1999/111199/23ms593.htm](http://www.ednmag.com/ednmag/reg/1999/111199/23ms593.htm); pp. 1-5

7 PARKnelco; N4000-6 Overview and product description; pp. 1-2

8 PARKnelco; N6000 and N6000SI Overview and product description; pp. 1-2

9 PARKnelco; N7000-2 HT Laminate and N7000-3 Prepeg Overview and product description; pp. 1-2

10 PARKnelco; N8000 Overview and product description; pp. 1-2

Examiner: \_\_\_\_\_

Date Considered: \_\_\_\_\_

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